



Programmable Edge Node with Hybrid Multi-core Processors

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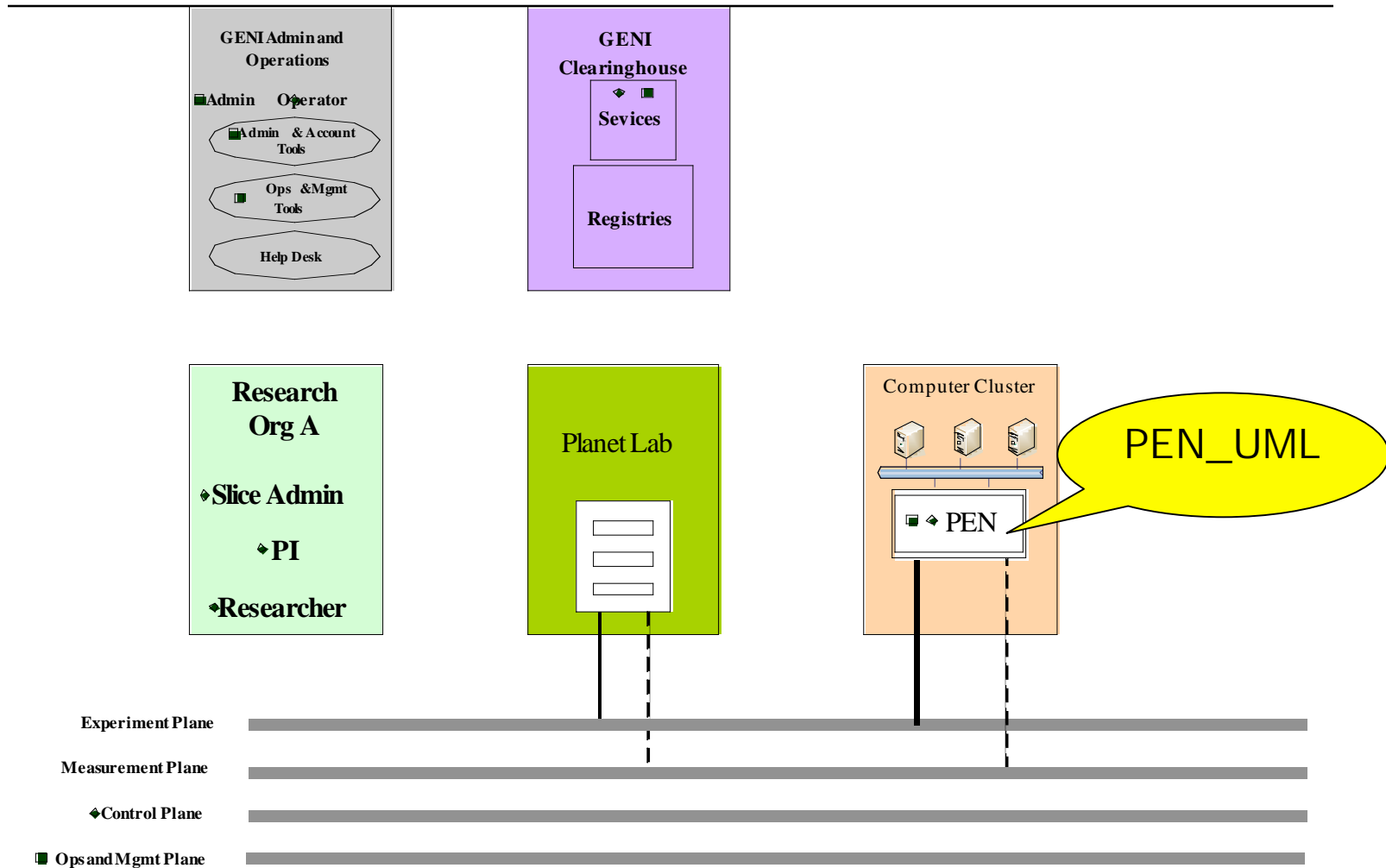
Outline

- Project Goals of PEN_UML
- How PEN_UML fits in GENI
- Architecture and Benefits of PEN_UML
- Preliminary Data
- Problems Under Study
- In 6-12 months

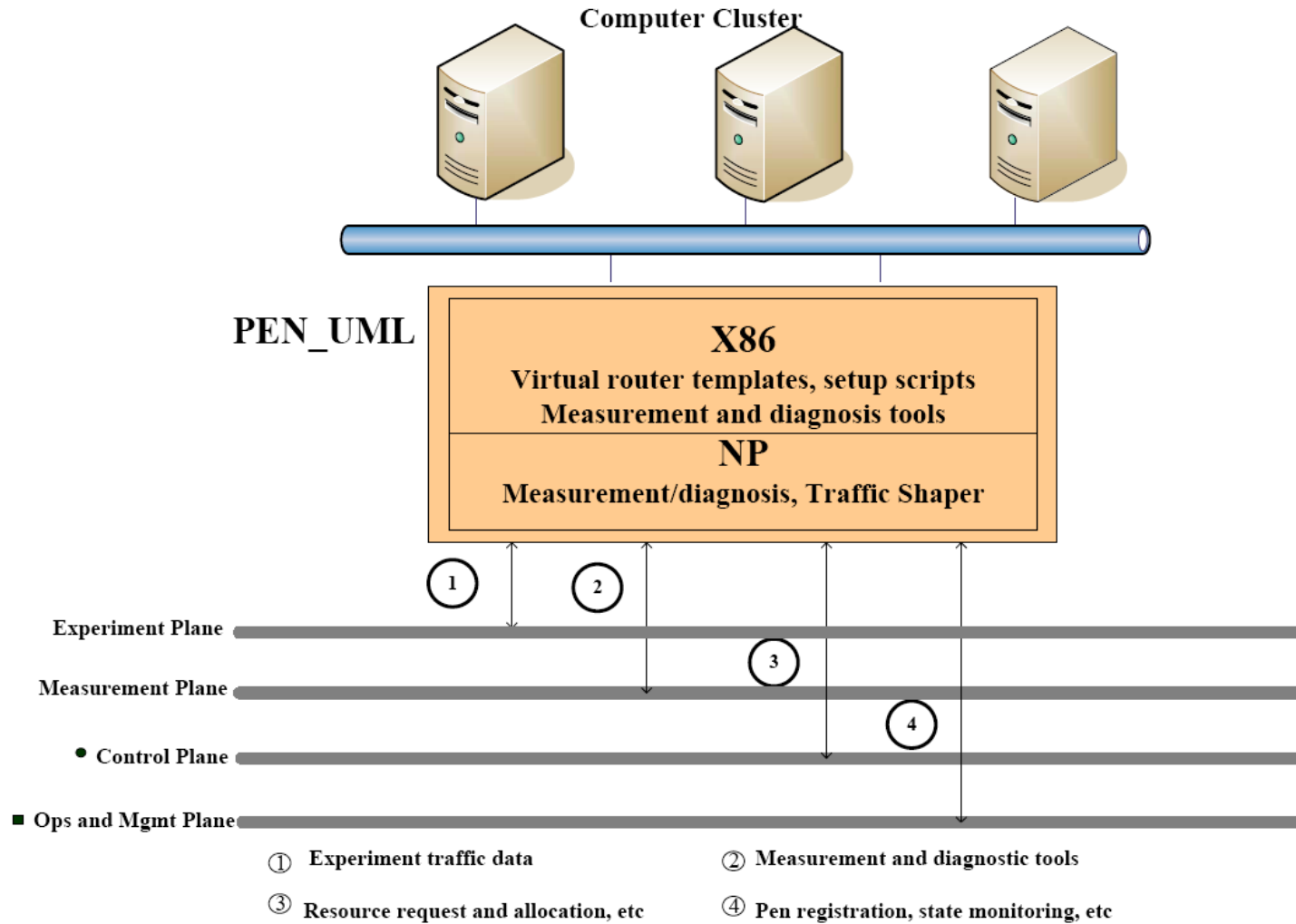
Project Goals of Programmable Edge Node at UMass Lowell (PEN_UML)

- Design and implement a Programmable Edge Node (PEN) with hybrid multi-core processors (x86 and network processors)
- Provide control & management, measurement, diagnosis tools
- Trial integration with a GENI control framework

How PEN_UML Fits In GENI

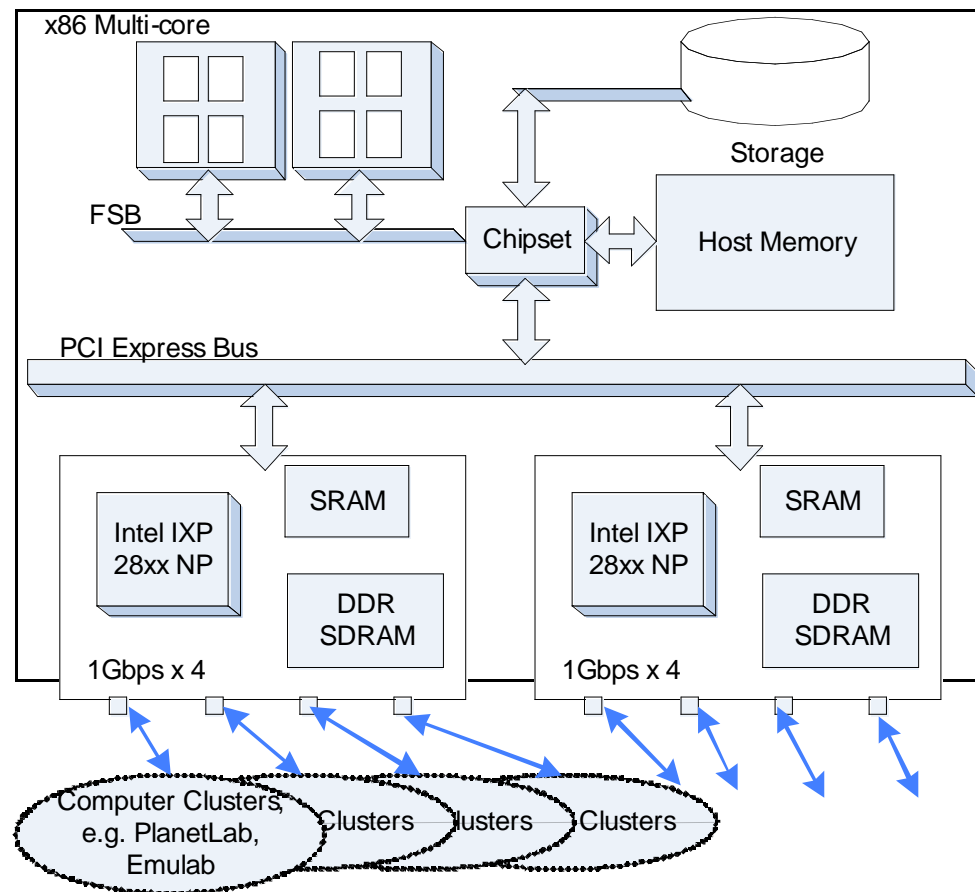


PEN_UML Operation Overview

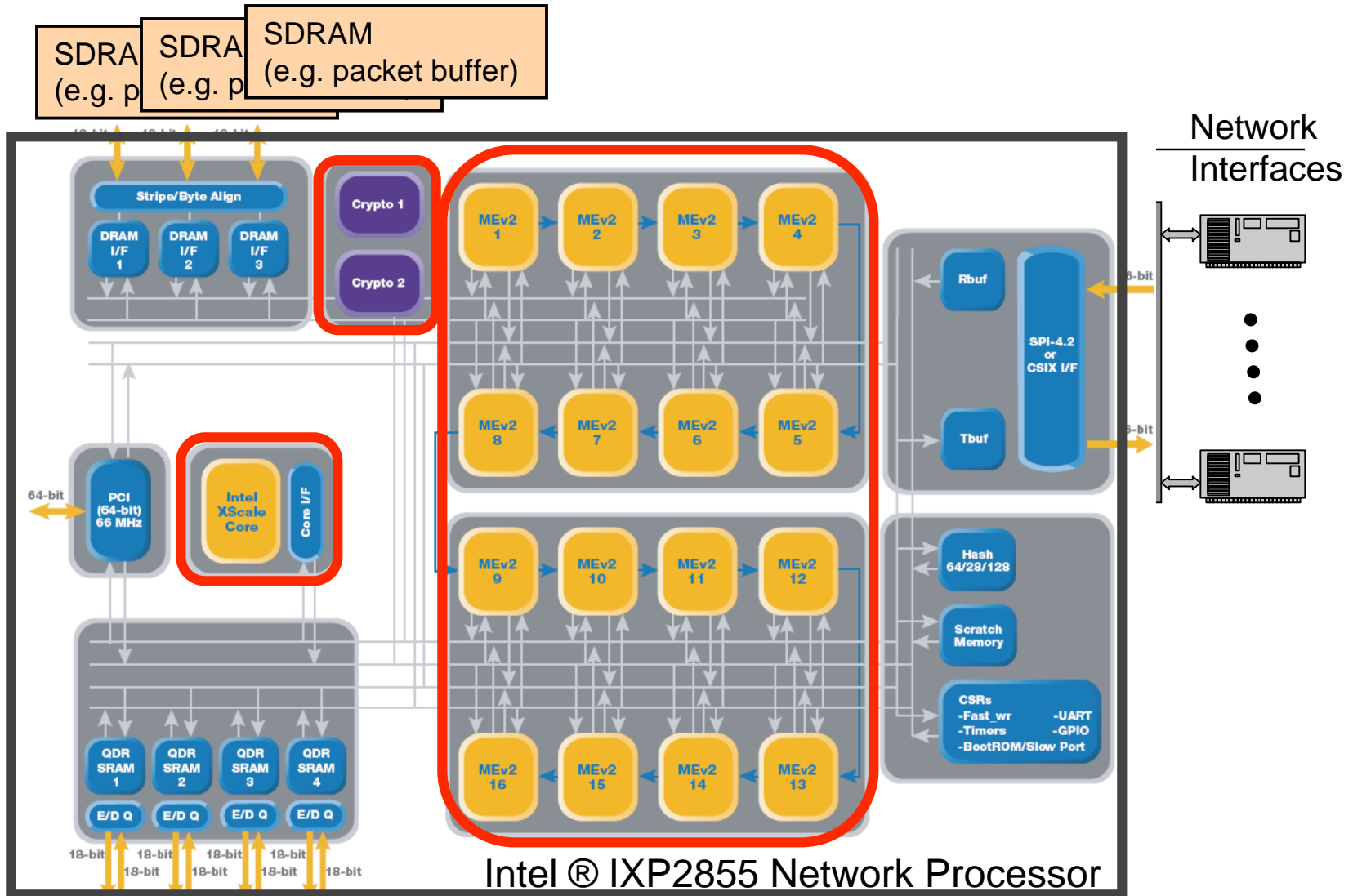


PEN_UML Hardware Overview

Programmable Edge Node
(hardware architecture with off-the-shelf components)

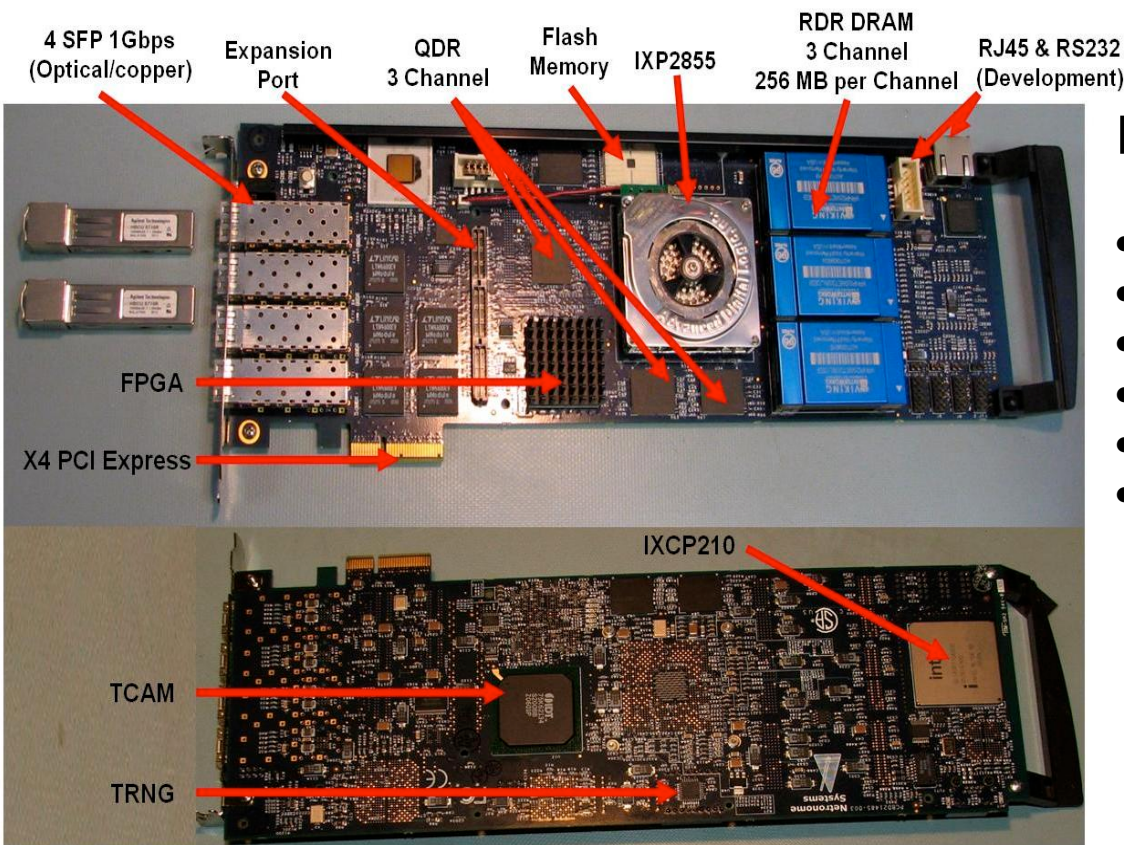


Programmable Network Processor Architecture



Netronome's Network Processor Card

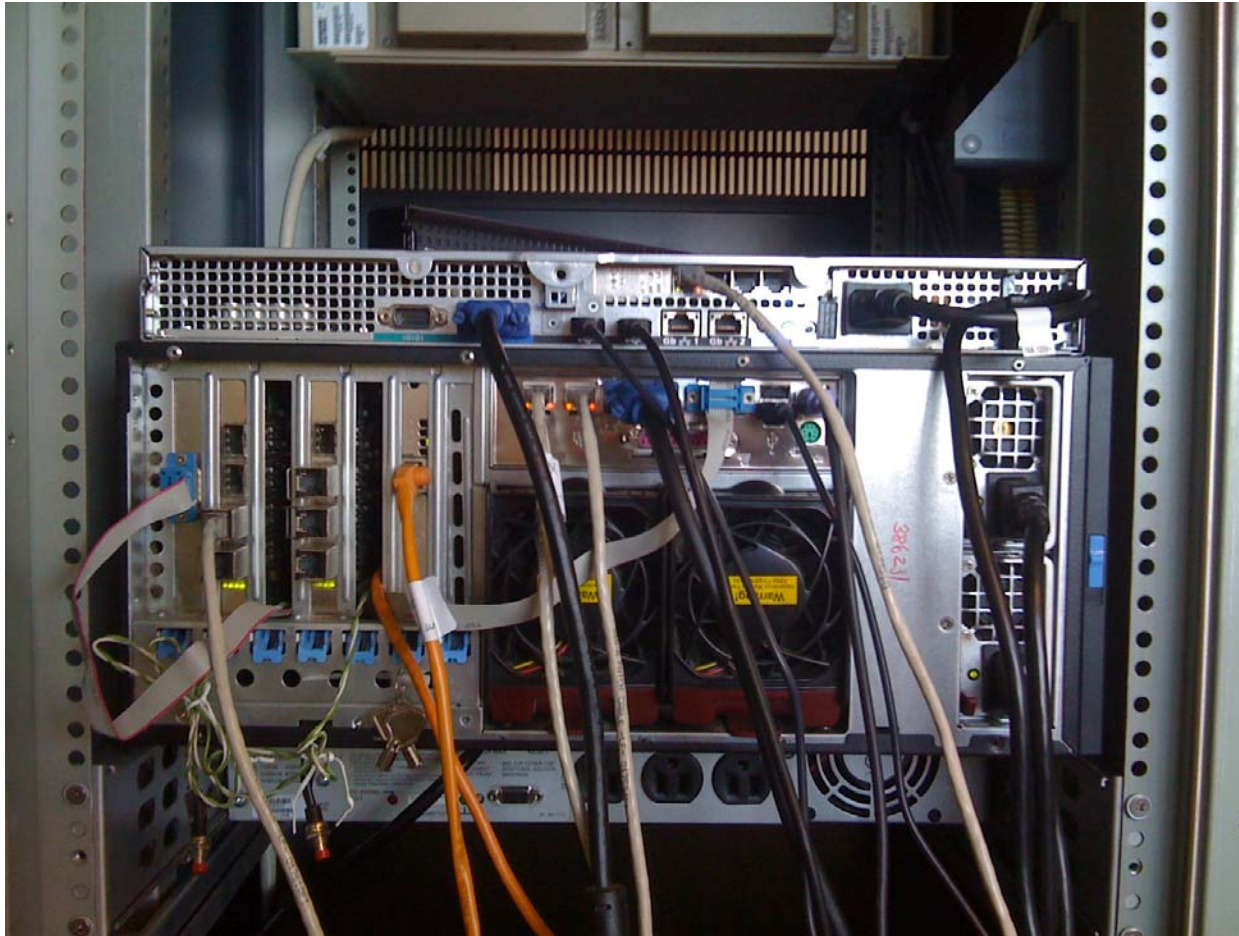
NFE: Hardware Overview



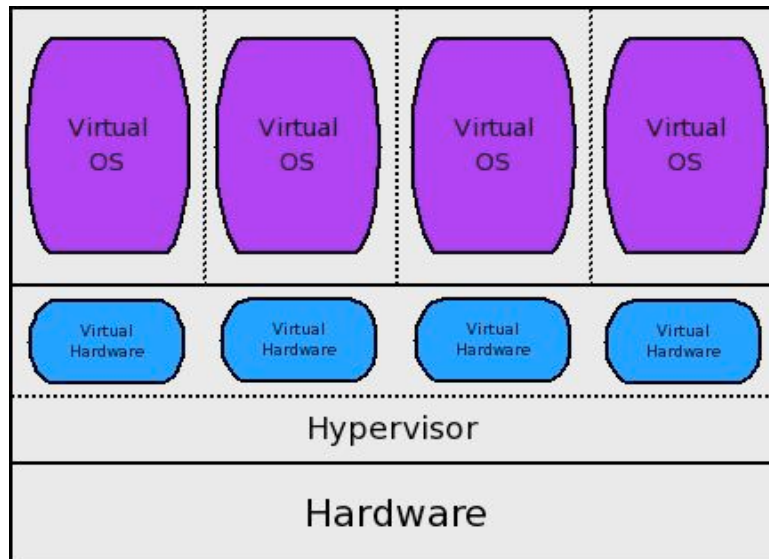
NFE-i8000 Specifications

- 1.4GHz IXP2855 NPU
- crypto units
- 4x1Gbps Ethernet ports
- 9Mb TCAM
- 40MB QDR2 SRAM
- 768MB RDRAM

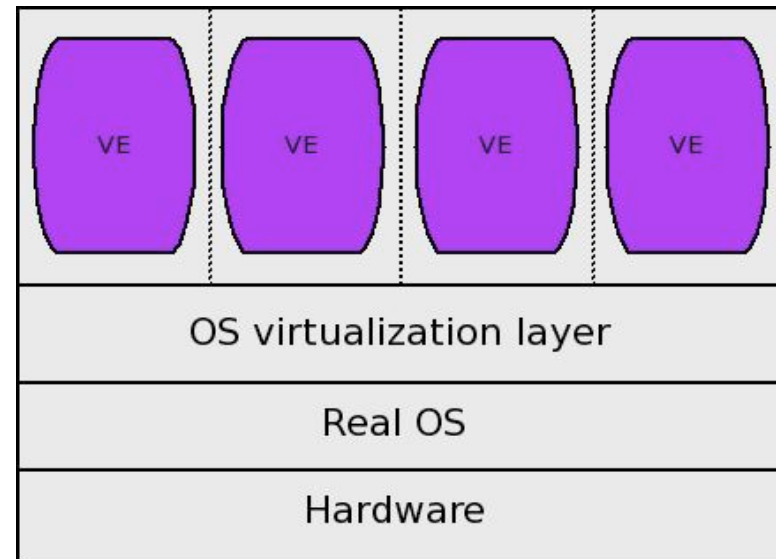
Picture of the Hardware



Virtualization Technologies

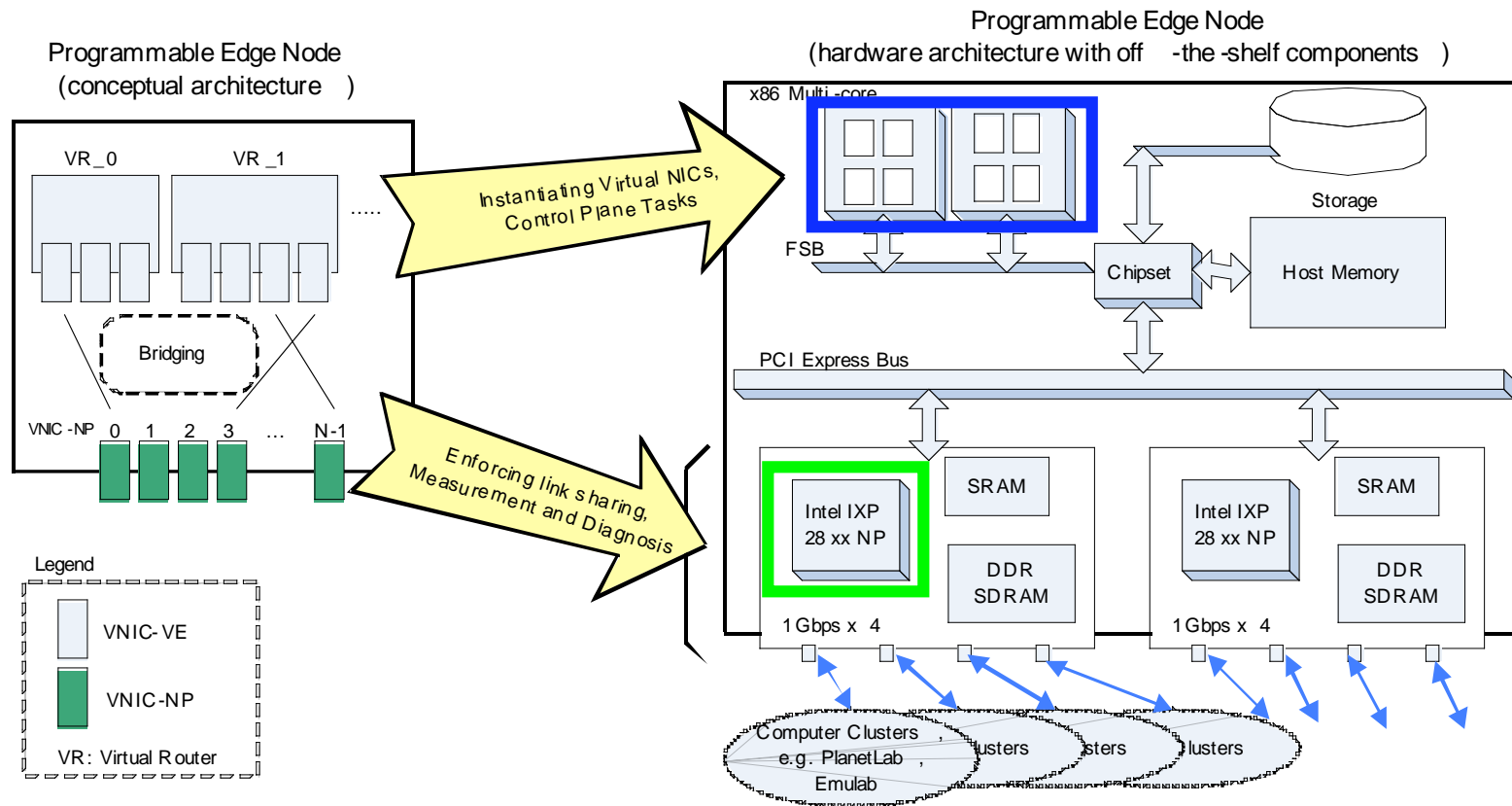


Full Virtualization
(e.g. VMWare, Xen)



OS Level Virtualization
(e.g. VServer, **OpenVZ**)

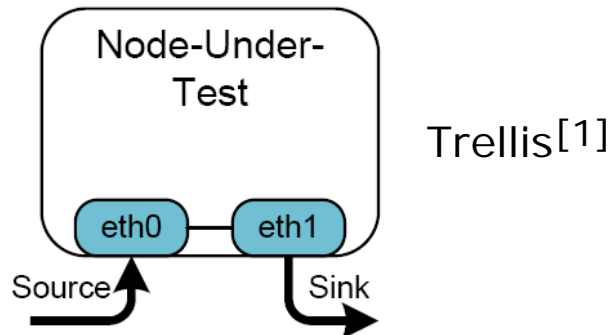
Virtual Routers Based on Hybrid Multi-core Processors of PEN_UML



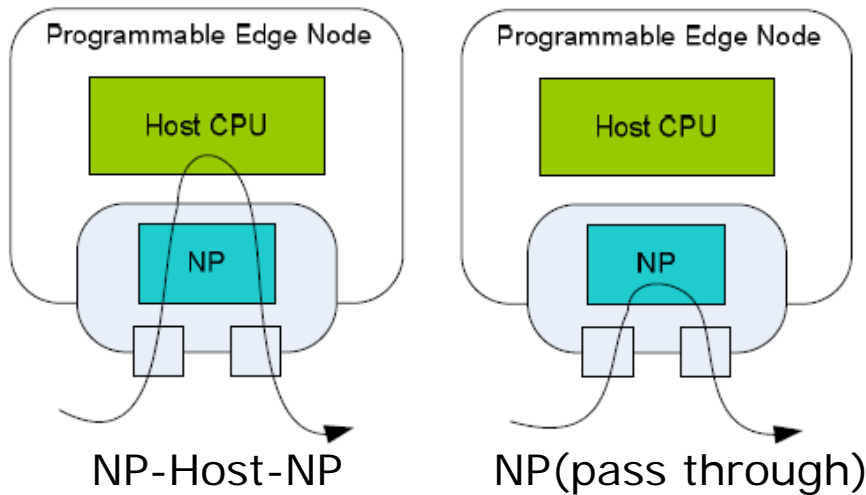
Benefits of PEN_UML

- Enforcement of Link Bandwidth Sharing
 - Host CPU supports virtual routers and NPs provide virtual NICs
 - NPs can offload traffic shaping from host CPU
- Isolation of Experiment and Measurement
 - Experiment measurement can be performed on NP
 - Host CPU carries out user experiments
- Flexible Number of Virtual NICs provided by NP
 - Virtual NICs on VEs can be one-to-one mapped to a VNIC-NP so that ARP broadcasts are avoided
- Acceleration of Complex Packet Processing Workloads
 - NPs can be programmed to accelerate heavy packet processing load

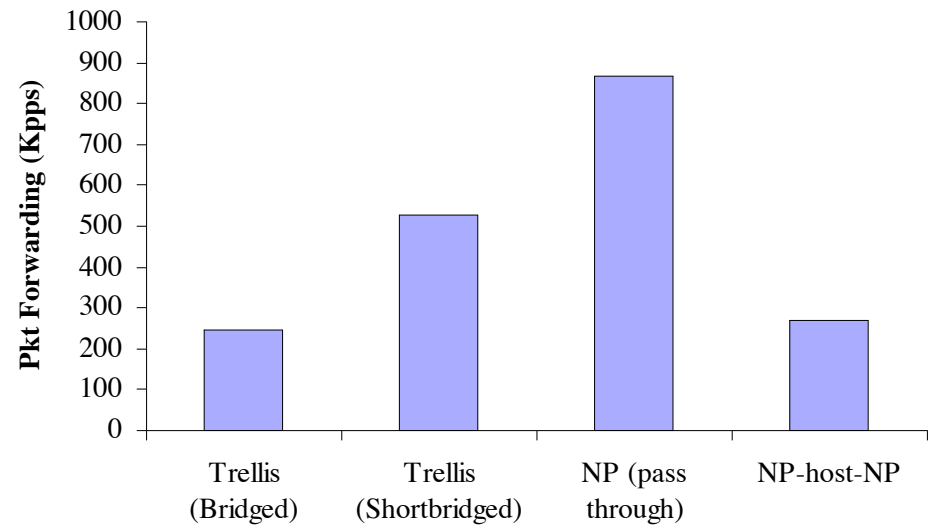
Performance Evaluation (I) - Packet Forwarding



[1] Sapan Bhatia, et al. Hosting virtual networks on commodity hardware. Technical Report GTCS-07-10, Georgia Tech, Jan 2008.



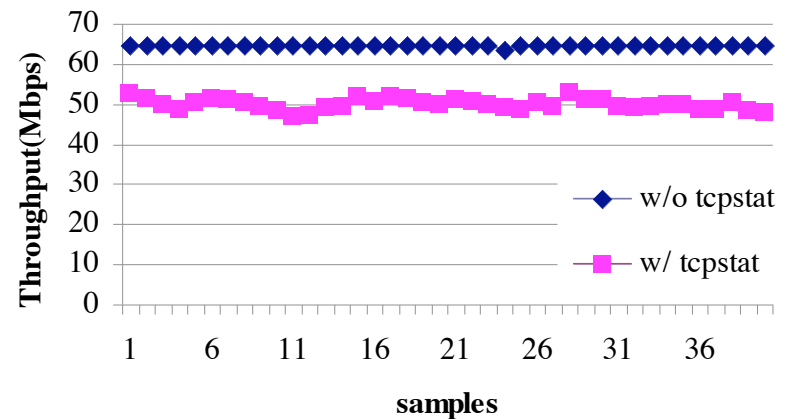
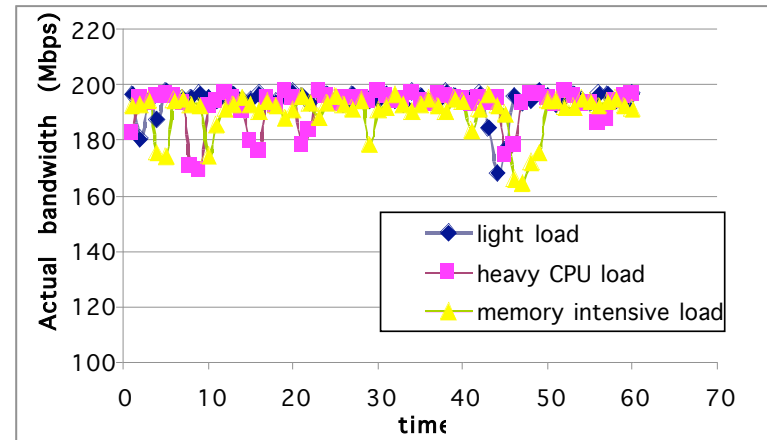
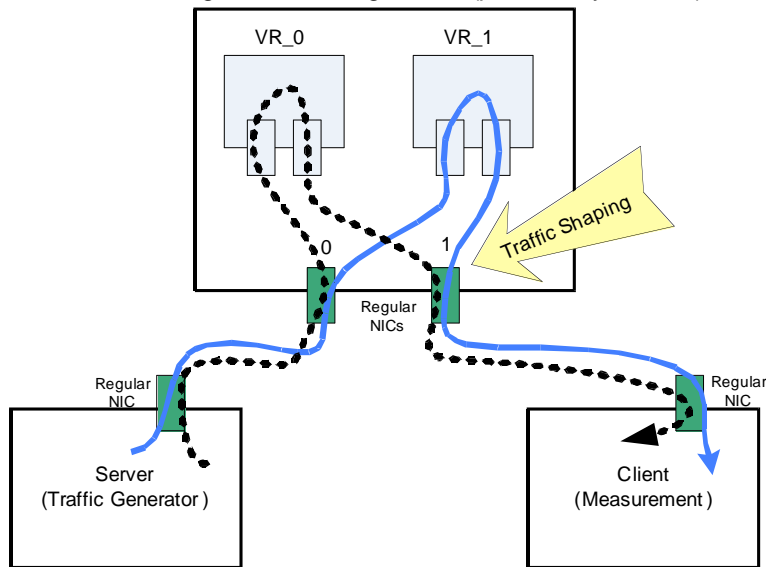
[2] Y. Luo and C. Zhang, The Design of A Programmable Network Edge Node with Hybrid Multi-core Processors for Virtual Networks, ICCCN, Aug 2008



Performance Evaluation (II) - Bandwidth Allocation Enforcement & Isolation of Experiment and Measurement

Conventional architecture w/o NP

Programmable Edge Node (preliminary testbed)

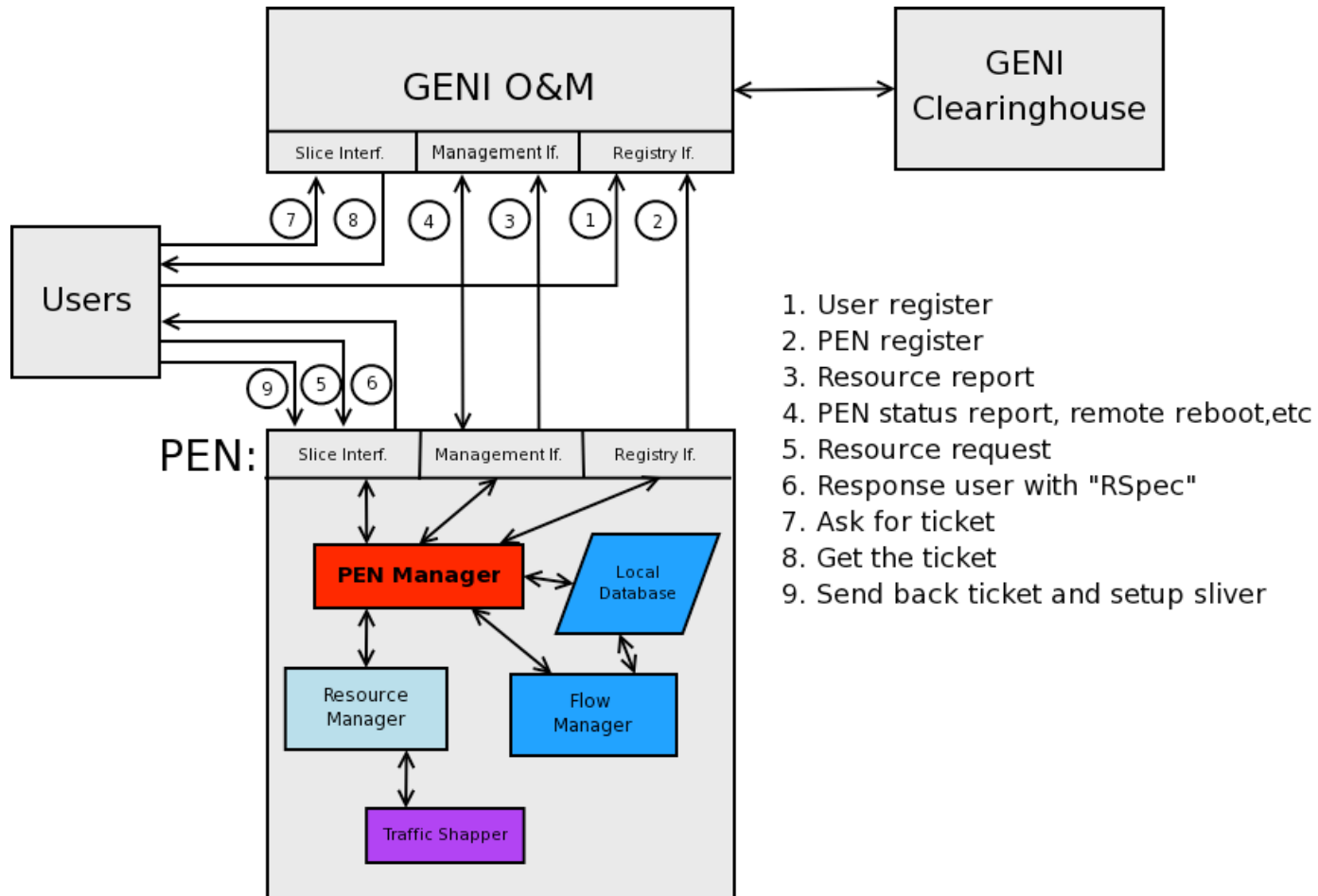


Y. Luo and C. Zhang, The Design of A Programmable Network Edge Node with Hybrid Multi-core Processors for Virtual Networks, IEEE ICCCN'08

Issues Under Study

- Control software and APIs
 - Develop generic control software and APIs to fit in a specified GENI control framework.
- Measurement and diagnostic tools
 - Develop measurement and diagnostic tools as elements of GIMS and user help tools within a specified GENI control framework
- Network bandwidth sharing
 - Implement traffic shapers on hybrid multi-core processors to enforce link sharing within a specific GENI control framework.

PEN with GENI O & M



In 6-12 Months ...

- Design and implementation
 - Virtual router templates & setup scripts
 - Control APIs and manager s/w
 - Measurement tools
 - Diagnosis tools
 - Traffic shapers on x86
- Trial integration with a control framework (e.g. ProtoGENI)
 - Conversation already started