

The Advanced Profile-Driven Testbed

Robert Ricci June 23, 2014



A platform for sharing research artifacts and environments

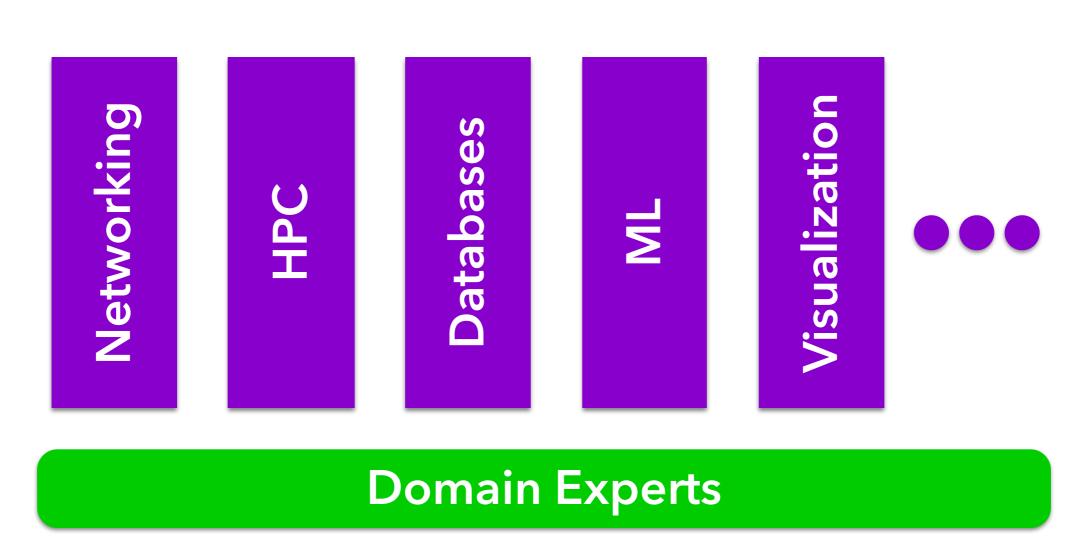
A facility for building testbeds tailored to specific domains

A choice...





Role for Domain Experts



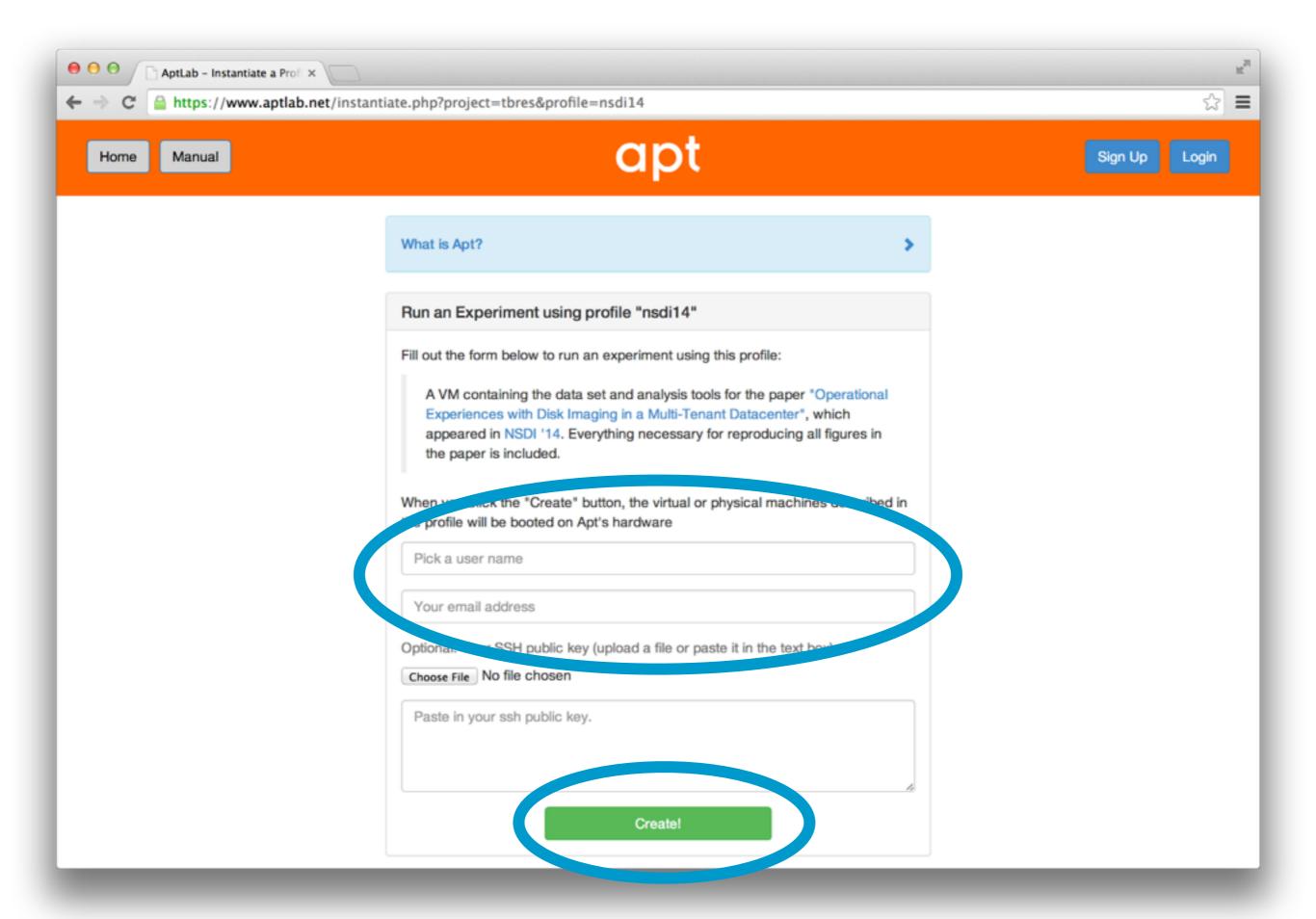
Infrastructure

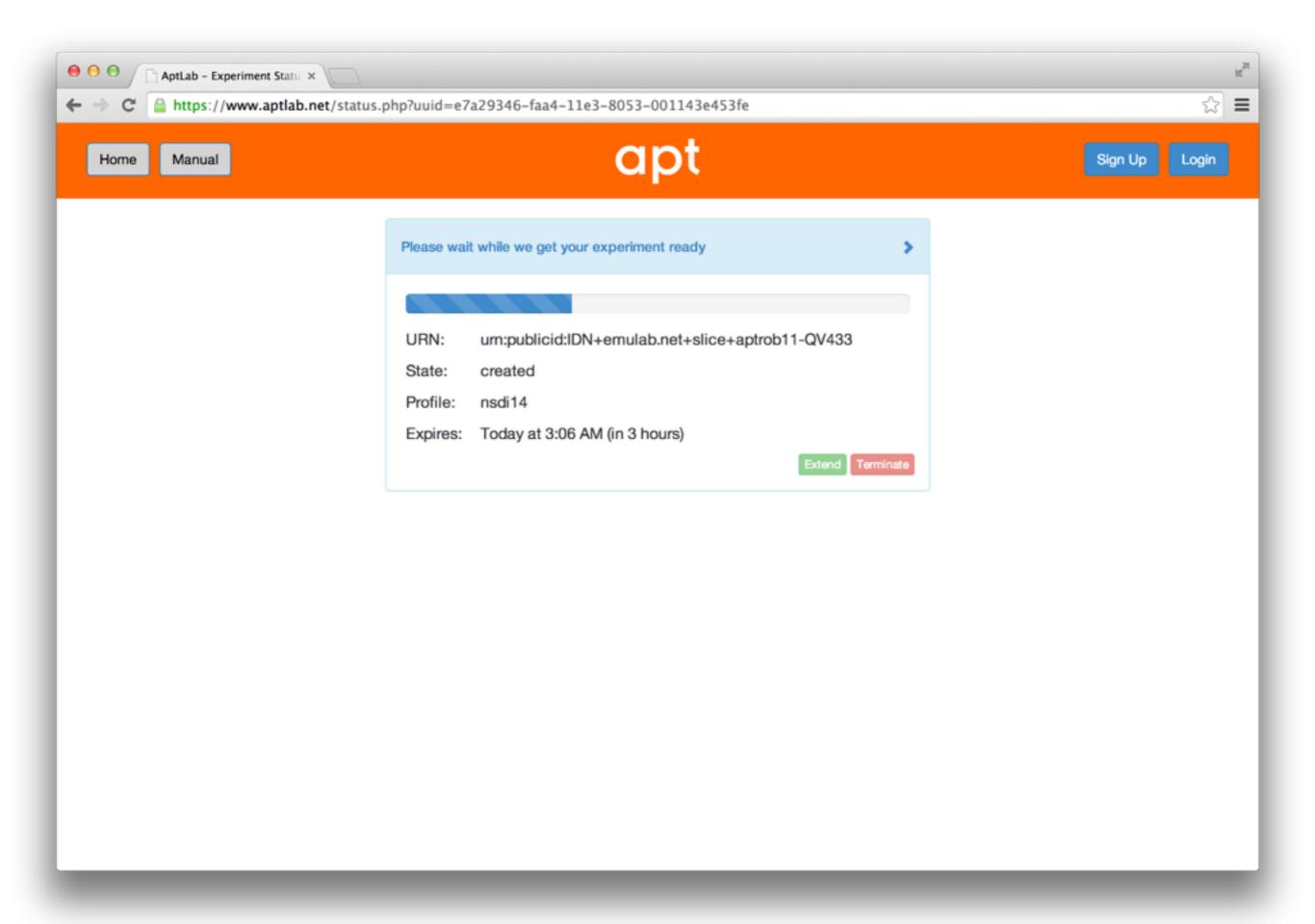
Profiles: Packaged experiments

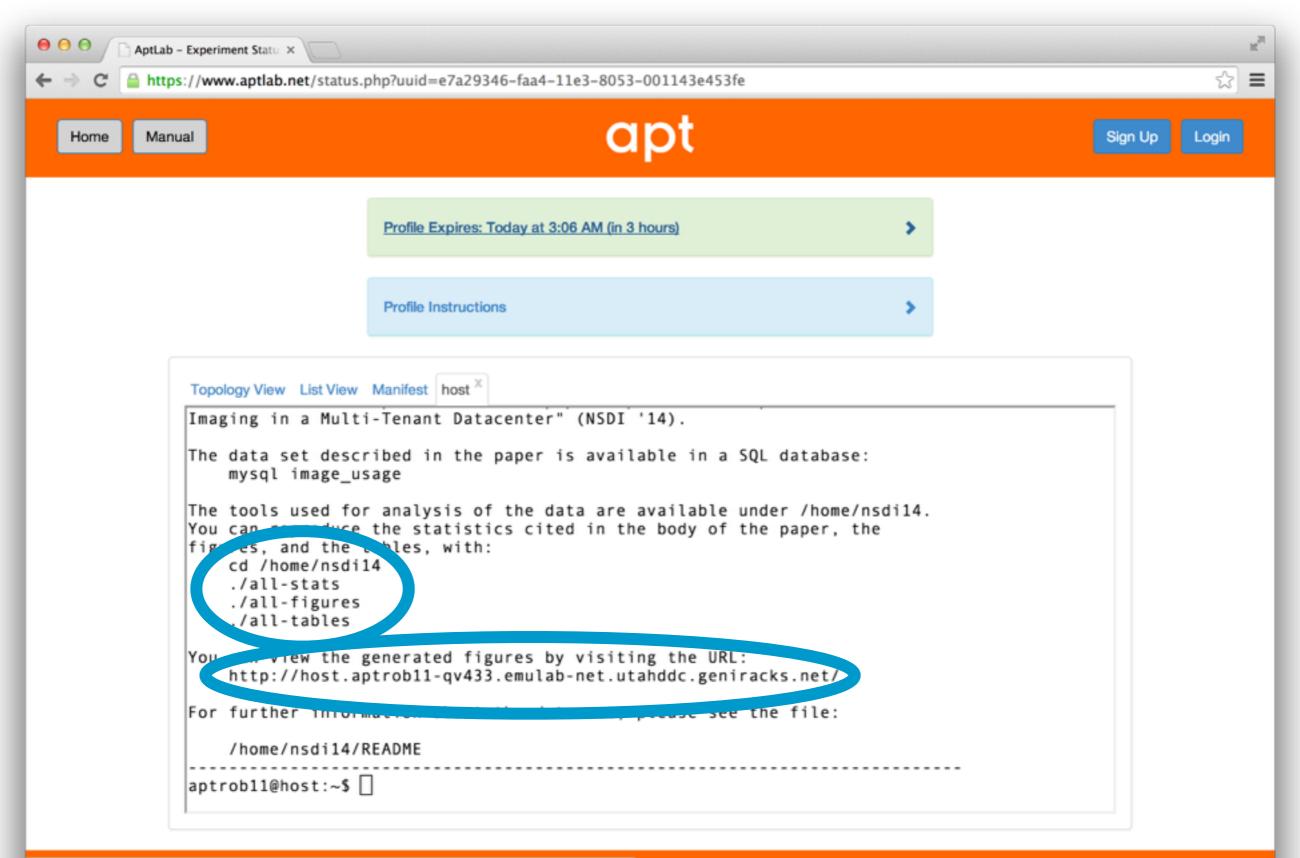
Packaged testbeds



http://aptlab.net/p/tbres/nsdi14









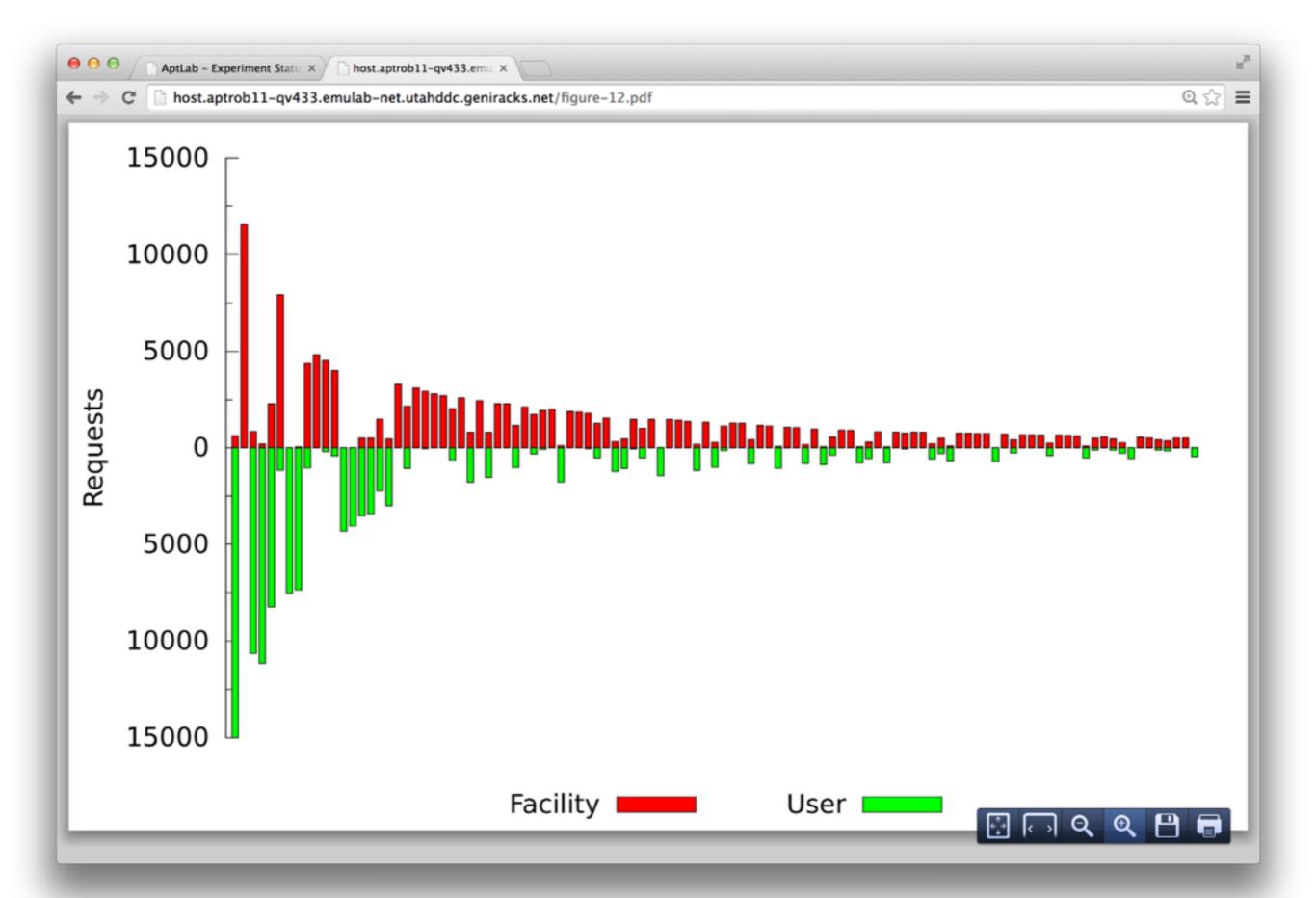
Operational Experiences with Disk Imaging in a Multi-Tenant Datacenter

Welcome to an APT profile instance for the paper "Operational Experiences with Disk Imaging in a Multi-Tenant Datacenter" (NSDI'14). This page is being served from a VM set up to replicate the experiment described in that paper. If you brought up this VM, you can log in to it via the profile page. If you did not bring it up, but would like to, you can instantiate one yourself.

Figures from the paper are available:

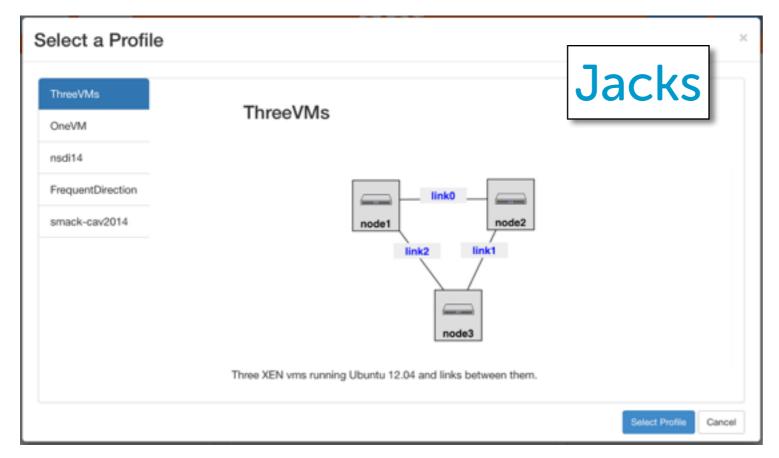
- Number of requests for facility and user images
- Lifespans of user images
- 3. Usage gap for user images
- 4. Histogram of similarity between images
- 5. Variation of facility image popularity over time
- 6. Daily working set size
- 7. Weekly working set size
- 8. Number of images used, for subsamples of the userbase
- 9. Daily working set size, for subsamples of the userbase
- 10. Image request distribution compared to exponential
- 11. Numbers of images users request
- 12. Profile of image types used by heaviest users
- 13. Percentage of requests for default, facility, and user images
- 14. Ratio of satisfied requests when free pool size is varied
- 15. Ratio of satisfied requests when reload rate is varied
- 16. Network traffic required to transfer image deltas

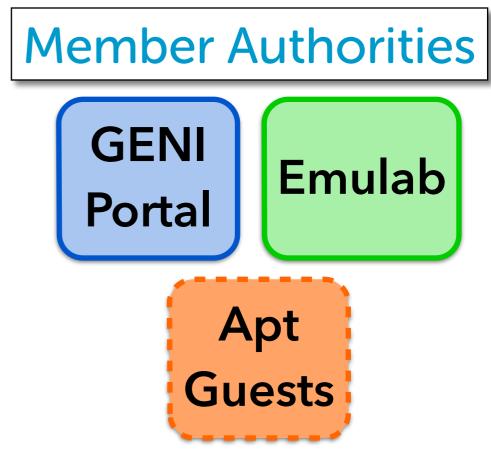
Those figures may be inspected, regenerated, or modified by logging in to the VM (as described above).



It's Just GENI*



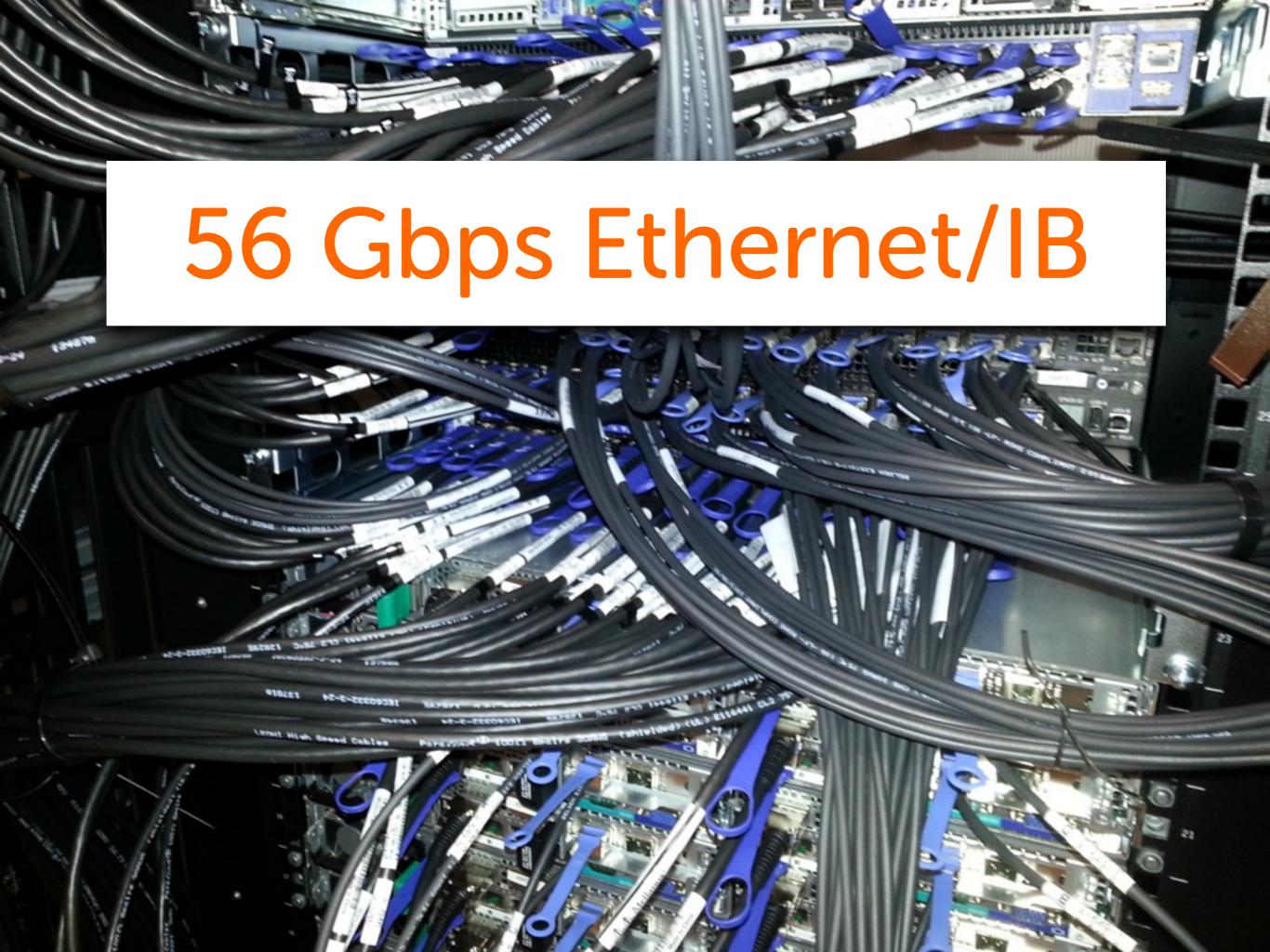




*with some PGENI-specific features







Early Users

SMACK: Decoupling Source Language Details from Verifier Implementations*

Zvonimir Rakamarić¹ and Michael Emmi²

School of Computing, University of Utah, USA zvonimir@cs.utah.edu
IMDEA Software Institute, Spain michael.emmi@imdea.org

Abstract. A major obstacle to putting software verification research into practice is the high cost of developing the infrastructure enabling the application of verification algorithms to actual production code, in all of its complexity. Handling an entire programming language is a huge endeavor that few researchers are willing to undertake; even fewer could invest the effort to implement a verification algorithm for many source languages. To decouple the implementations of verification algorithms

Formal Software Verification

interpretation. Our initial experience in verifying C-language programs is encouraging: SMACK is competitive in SV-COMP benchmarks, is able to translate large programs (100 KLOC), and is being used in several verification research prototypes.

1 Introduction

This is a reformatted version of the paper that appears in SIGCOMM's proceedings

Using RDMA Efficiently for Key-Value Services

Anuj Kalia Michael Kaminsky[†] David G. Andersen
Carnegie Mellon University [†]Intel Labs
{akalia,dga}@cs.cmu.edu michael.e.kaminsky@intel.com

ABSTRACT

This paper describes the design and implementation of HERD, a key-value system designed to make the best use of an RDMA network. Unlike prior RDMA-based key-value systems, HERD focuses its design on reducing network round trips while using efficient RDMA primitives; the result is substantially lower latency, and throughput that saturates modern, commodity RDMA hardware.

HERD has two unconventional decisions: First, it does not

table access? To answer this question, we first evaluate the performance that, with sufficient attention to engineering, can be achieved by each of the RDMA communication primitives. Using this understanding, we show how to use an unexpected combination of methods and system architectures to achieve the maximum performance possible on a high-performance RDMA network.

Our work is motivated by the seeming contrast between the fundamental time requirements for cross-node traffic vs. CPU-to-memory lookups, and the designs that have recently

Infiniband Key/Value Store

with 5 µs average latency. Notably, for small key-value items, our full system throughput is similar to native RDMA read throughput and is over 2X higher than recent RDMA-based key-value systems. We believe that HERD further serves as an effective template for the construction of RDMA-based datacenter services.

RDMA read bypasses many potential sources of overhead, such as servicing interrupts and initiating control transfers, which involve the host CPU. In this paper, we show that there is a better path to taking advantage of RDMA to achieve high-throughput, low-latency key-value storage.

A challenge for both our and prior work lies in the lack of richness of RDMA operations. An RDMA operation can only





Hari Sundar

home research teaching blog code

Big Data Computer Systems

Fall 2014

Mon,Wed 1:25pm-2:45pm MEB 3147

Catalog number: CS 5965/6995

Overview

The exponential increase in the quantity and quality of measurements and data holds tremendous promise for data-driven scientific discovery. However, much of this data remains

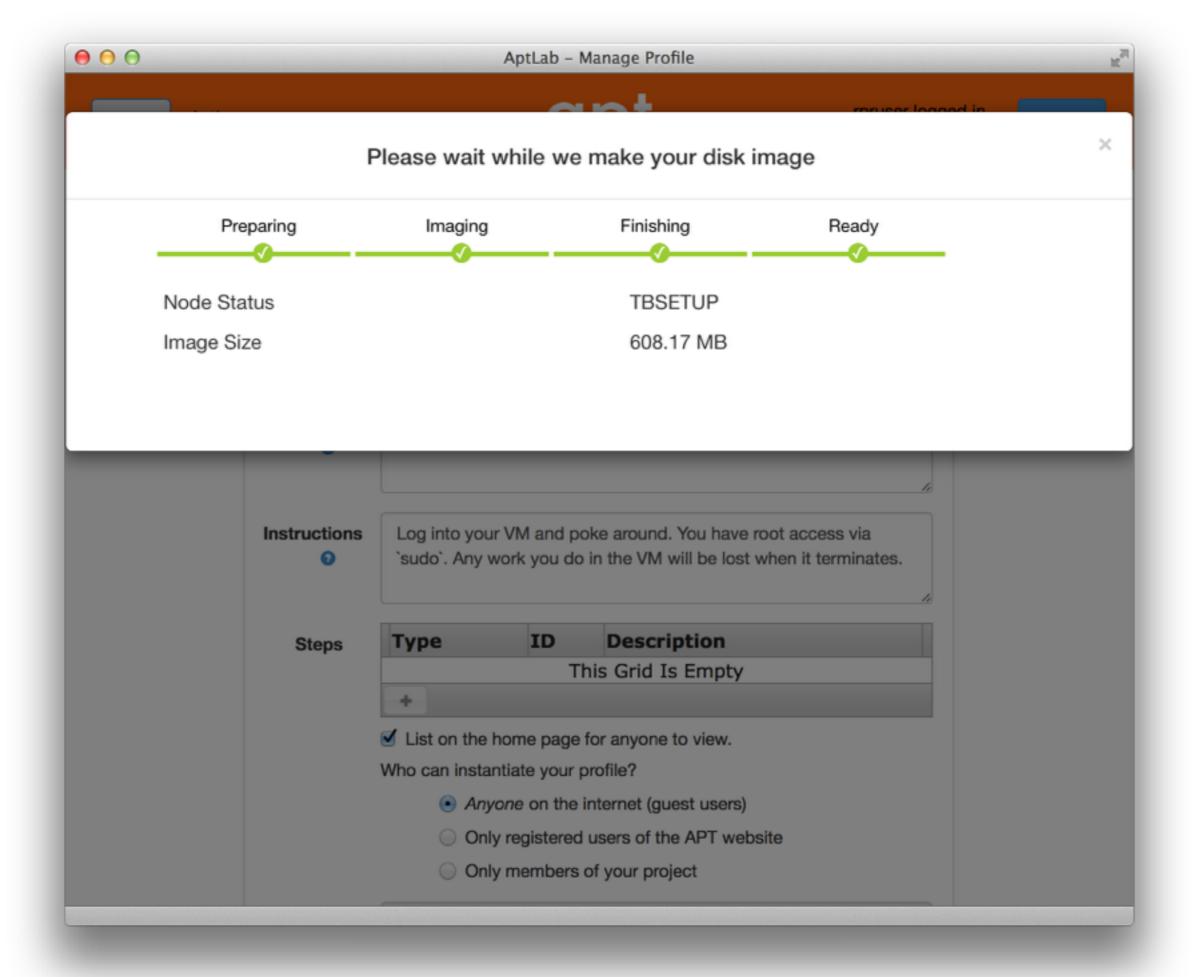
Big Data Class

course will focus on the computer systems aspect—for instance, how various parts of a big data semputer system (hardware system software, and applications) are put together? What a high performance, scalability, and reliability in

system from the <u>Flux</u> research group.

Hadoop Ethernet

course but CS 3505 or equivalent programming structor if you are not sure whether you possess ne projects might require knowledge of Numerical nts are not required to have this background, but



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